J1011 U.S. PTO
09/938106
08/23/01

ISSUE CLASSIFICATION	Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED	PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/938106		257	685	2811	

APPLICANTS: James Derderian

TITLE: Assemblies including stacked semiconductor devices separated by discrete conductive elements therebetween, packages including the assemblies, and methods

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL CLASS	ORIGINAL SUBCLASS

INTERNATIONAL CLASSIFICATION

CROSS REFERENCE(S) CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)

☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

_____ (Assistant Examiner) _____ (Date)

NOTICE OF ALLOWANCE MAILED

☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____

_____ (Primary Examiner) _____ (Date)

ISSUE FEE

Amount Due	Date Paid

☐ The terminal ____months of this patent have been disclaimed.

_____ (Legal Instruments Examiner) _____ (Date)

ISSUE BATCH NUMBER

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